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For: METHOD FOR LIGHTING FLAT
FLUORESCENT LAMP

Examiner: Unassigned

SUBMISSION OF VERIFIED ENGLISH TRANSLATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Attached is an executed Certificate of Verification, along with the verified English translation of Japanese International Application No. PCT/JP2005/001004, which was filed on September 26, 2005 in the U.S. Patent and Trademark Office.

Dated: November 29, 2005

Respectfully submitted,



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CERTIFICATE OF VERIFICATION

I, Su Hyun LEE of 648-23 Yeoksam-dong, Kangnam-ku, Seoul, Korea state that the attached document is a true and complete translation to the best of my knowledge of the Korean-English language and that the writings contained in the following pages are correct English translations of the specifications and claims of the International Patent Application No. PCT/JP2005/001004.

Dated this 7th day of November 2005

Signature of translator: _____

A handwritten signature in black ink, appearing to be "Su Hyun LEE", written over a horizontal line.

Su Hyun LEE

METHOD FOR LIGHTING FLAT FLUORESCENT LAMP

TECHNICAL FIELD

The present invention relates to a method for lighting a flat fluorescent lamp,
5 in which the flat fluorescent lamp is provided and illuminated in a rear surface of an LCD device, to obtain the brightness in a display surface of the LCD device, and more particularly, to a method for lighting a flat fluorescent lamp of a large-sized backlight suitable for a large-sized liquid crystal television.

BACKGROUND ART

10 In general, a backlight unit is comprised of a plurality of cold cathode fluorescent lamps and a light-guiding plate of acrylate. However, the backlight unit may have the following disadvantages such as the loss of light due to the light-guiding plate and the increasing number of cold cathode fluorescent lamps according to the large-sized LCD device.

15 As disclosed in patent documents 1, 2 and 3, a flat fluorescent lamp is formed of one lamp, which functions as a backlight. The flat fluorescent lamp is very useful for a large-sized backlight unit in that it can be resistant to an external air pressure in a large size, it can obtain a uniform surface luminance, and it can be illuminated brightly even at a low temperature. In this flat fluorescent lamp having a plurality of groups
20 of electrodes, a lighting method is the problem which confronts us.

Patent Document 1: Patent Application No. 2003-436375

Patent Document 2: Patent Application No. 2003-428313

Patent Document 3: Patent Application No. 2003-420114

DISCLOSURE OF THE INVENTION

25 Accordingly, the present invention is directed to a method for lighting a flat fluorescent lamp that substantially obviates one or more problems due to limitations and disadvantages of the related art.

An object of the present invention is to provide a method for lighting a flat fluorescent lamp to prevent the discharge interference (dispersing in fluorescent

discharge) between adjacent groups of cylindrical electrodes when lighting the plurality of groups of cylindrical electrodes through introduction wires by applying a voltage.

Additional advantages, objects, and features of the invention will be set forth in part in the description which follows and in part will become apparent to those having
5 ordinary skill in the art upon examination of the following or may be learned from practice of the invention. The objectives and other advantages of the invention may be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

10 To achieve these objects and other advantages and in accordance with the purpose of the invention, as embodied and broadly described herein, in a method for lighting a flat fluorescent lamp, the flat fluorescent lamp includes a front glass substrate provided with a plurality of grooves formed in the predetermined portions except the circumference of the front glass substrate and the portion for forming
15 members such as cylindrical electrodes, and a fluorescent substance coating film formed on the inner surface of the front glass substrate except the circumference; a rear glass substrate provided with the plurality of grooves formed in the predetermined portions except the circumference of the rear glass substrate and the portion for forming members such as the cylindrical electrodes, and a fluorescent substance
20 coating film formed on the inner surface of the rear glass substrate except the circumference; and a sealed body formed by welding the circumference of the front and rear glass substrates in state of being inserted to a glass frame having an exhaust pipe after coupling the front and rear glass substrates, on providing the sealed body, an exhaust pipe is formed to the upper side of the glass frame, a plurality of introduction
25 wires for lighting lamp being welded to a plurality of groups of cylindrical electrodes are formed to the both lateral sides of the glass frame, and introduction wires for a heater, connected with a heater wires and a plate-type spring, are formed to the lower side of the glass frame.

To realize the method for lighting the flat fluorescent lamp for preventing the

discharge interference (dispersing in fluorescent discharge) between adjacent groups of cylindrical electrodes, an A.C. voltage is applied to one or two groups of cylindrical electrodes through the introduction wires for lighting lamp in state of being not applied to adjacent one or two groups of cylindrical electrodes, so the plurality of groups of cylindrical electrodes are sequentially switched on and off in a time-division method at a speed not to generate the flicker of lamp.

It is to be understood that both the foregoing general description and the following detailed description of the present invention are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

10 BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention and together with the description serve to explain the principles of the invention.

15 In the drawings:

FIG. 1 illustrates a method for lighting a flat fluorescent lamp according to the preferred embodiment of the present invention.

BEST MODE FOR CARRYING OUT THE INVENTION

Reference will now be made in detail to the preferred embodiments of the present invention, examples of which are illustrated in the accompanying drawings. Wherever possible, the same reference numbers will be used throughout the drawings to refer to the same or like parts.

Hereinafter, a method for lighting a flat type fluorescent lamp according to the preferred embodiment of the present invention will be described with reference to the accompanying drawings.

FIG. 1 illustrates a method for lighting a flat fluorescent lamp according to the preferred embodiment of the present invention.

Referring to FIG. 1, a flat type fluorescent lamp according to the preferred embodiment of the present invention is provided with a front glass substrate of a

rectangular shape and a rear glass substrate of a rectangular shape, wherein the front glass substrate 1 is coupled with the rear glass substrate 2. Then, the coupled front and rear glass substrates 1 and 2 are inserted to a glass frame 3, wherein members such as exhaust pipe is provided to the glass frame. As a result, it is possible to form
5 a sealed body with the front and rear glass substrates and the glass frame.

Specifically, the front glass substrate 1 is provided with a plurality of rows of grooves (not shown) for reinforcement of the glass substrates. At this time, the grooves are formed in the predetermined portions except the circumference of the front glass substrate and the portion for forming members such as cylindrical electrodes.
10 Also, a fluorescent substance (not shown) is coated on the inner surface of the front glass substrate except the circumference.

The rear glass substrate 2 is provided with a plurality of rows of grooves (not shown) in parallel. At this time, the grooves are formed in the predetermined portions except the circumference of the rear glass substrate and the portion for
15 forming members such as the cylindrical electrodes. Also, a fluorescent substance (not shown) is coated on the inner surface of the rear glass substrate except the circumference.

When the sealed body is provided in state of positioning the grooves in horizontal, an exhaust pipe 4 is provided in an upper side of the glass frame 3. Also,
20 a plurality of introduction wires for lighting the lamp 5 are provided to the both lateral sides of the glass frame 3, wherein a plurality of groups of cylindrical electrodes 6 are welded to the introduction wires for lighting the lamp 5. Then, a heater wire 8 and a plate-type spring 9 are provided in the lower side of the glass frame 3, wherein introduction wires for heater 7 are connected with the heater wire 8 and the plate-type
25 spring 9.

In the aforementioned flat fluorescent lamp according to the preferred embodiment of the present invention, an A.C. voltage is applied to the plurality of groups of cylindrical electrodes 6 through the introduction wires for lighting the lamp 5 from a lighting system 10, whereby the groups of cylindrical electrodes 6 are in the

switching-on state. In state of providing the A.C. voltage to one or two groups of cylindrical electrodes, the A.C. voltage is not applied to adjacent one or two groups of cylindrical electrodes, whereby the adjacent one or two groups of cylindrical electrodes are maintained in the turning-off state. As a result, the plurality of groups of cylindrical electrodes are sequentially switched on and off in a time-division method at a speed not to generate the flicker of lamp, so that it is possible to prevent the discharge interference between the adjacent groups of cylindrical electrodes. Also, the frequency of the A.C. voltage for switching-on the lamp is about 50KHz. For example, the plurality of groups of cylindrical electrodes are sequentially switched on and off at a speed of about 100Hz, thereby preventing the flicker in the luminance of the LCD device.

FIG. 1 illustrates the lighting system 10 in the lighting method of the flat fluorescent lamp according to the preferred embodiment of the present invention, wherein the lighting system 10 includes an A.C. voltage generating circuit and a time-division lighting circuit. That is, the A.C. voltage is applied to the plurality of groups of the cylindrical electrodes 6 through the introduction wires for lighting the lamp 5 in the time-division method. Under control of the lighting system 10, the A.C. voltage is applied to one group of cylindrical electrodes from the time-division lighting circuit in state of being not applied to the adjacent group of cylindrical electrodes. According to the time-division method, one group of cylindrical electrodes is in the switching-on state, and the adjacent one group of cylindrical electrodes is in the switching-off state in the speed not to generate the flicker.

INDUSTRIAL APPLICABILITY

Accordingly, the method for lighting the flat fluorescent lamp according to the present invention has the following advantages.

As the competition in development for the LCD device becomes keen in Korea, Japan and Taiwan, the industry fields of the LCD device and the backlight unit have been developed and researched actively. The method for lighting the flat fluorescent lamp according to the present invention is very useful in many ways.

It will be apparent to those skilled in the art that various modifications and variations can be made in the present invention without departing from the spirit or scope of the inventions. Thus, it is intended that the present invention covers the modifications and variations of this invention provided they come within the scope of

5 the appended claims and their equivalents.